

W1 Xmega processor Module

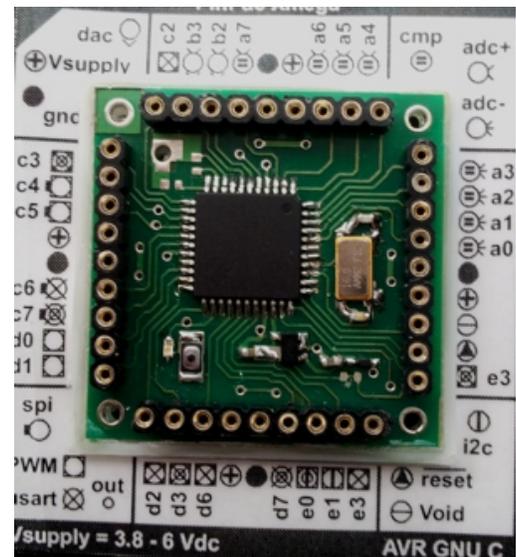
The Xmega module provides instrumentation inputs, capable of reading signals as low as 4 micro volts at 2 million samples per second, it features DMA, event driven subsystem, number of PWM outputs and interrupts on virtually every pin. Many communication channels, USART, I2C, SPI and etc. It is the mixed signal engineer's dream. Further information can be found in the manual from the chip developer Atmel. Note that manuals are two, one providing specifics and the other general information about this family of microcontrollers. Xmega / XmegaGeneral the user needs both.



Hx7 I/O map: Pin numbers **1 - 44** correspond to the Atxmega 128A48, 64A4U/ 32A4U and 16A4U for further information go to atmel.com and view the datasheet for these micro controllers.

Xmega Legend overlay

Xmega			
dac +Vsupply gnd c3 c4 c5 c6 c7 d0 d1 spi PWM usart Vsupply = 3.8 - 6 Vdc	13 14 15 16 17 20 21 22 23 24 27 28 29 32	spi: serial peripheral interface adc+: analog input positive adc-: analog input negative cmp: comparator inputs dac: analog output i2c: two wire interface void: don't use this pin PWM: pulse width modulation and timer outputs reset Void	cmp adc+ adc- a3 a2 a1 a0 e3 i2c
	12 7 6 3 2 1 44		
	c2 b3 b2 a7 a6 a5 a4 d2 d3 d6 d7 e0 e1 e2		
			AVR GNU C



The legend on the right describes the pin functions of the xmega. The mid pin in every row is ground or negative common return. Clockwise to the ground pin is the positive supply. Pins are labeled using symbols that refer to the alternative function the pin offers.

Note: Symbol functions description found on 4 corners